Notice of References Cited

Application/Control No.

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Examiner

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Art Unit
Page 1 of 1

U.S. PATENT DOCUMENTS

				U.S. PATENT DOCUM	LINIO	
*		Document Number Country Code-Number-Kind Code	Date MM-YYYY		Name	Classification
	- A	US-5,622,880	04-1997	Burr et al.		438/194
•	В	US-4,939,390	07-1990	Coe, T.		307/450
	С	US-				
	D	US-				
	E	US-				
	F	US-	}			
	G	US-				
	Н	US-				
	1	US-				
	J	US-				
	К	US-				
	L	US-				
	М	US-				
			· 1	FOREIGN PATENT DOC	UMENTS	
*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP406021451A	01-1994	Japan	Sase, Y.	H01L029/784
	0					
	Р					
	α					
	R					
	s					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	U	S.M. Sze, :Physics of Semiconductor Devices" John Wiley and Sons, inc., New York 1981, second edition, pages 32-33.				
	٧	S. Wolf, "Silicon Processing for the VLSI Era", Volume 2 -Process Integration, Lattice Press, Sunset Beach, California (USA), 1990 (ISBN: 0-961672-4-5), pages 300-301, 385, 388, 400 and 414-415.				
	w					
	Х					

"A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.